

CNT-Based MEMS/NEMS Gas Ionizers for Portable Mass Spectrometry Applications

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Abstract—We report the fabrication and experimental characterization of a carbon nanotube (CNT)-based MEMS/NEMS electron impact gas ionizer with an integrated extractor gate for portable mass spectrometry. The ionizer achieves low-voltage ionization using sparse forests of plasma-enhanced chemical-vapor-deposited CNTs as field emitters and a proximal extractor grid with apertures aligned to the CNT forests to facilitate electron transmission. The extractor gate is integrated to the ionizer using a high-voltage MEMS packaging technology based on Si springs defined by deep reactive ion etching. The ionizer also includes a high-aspect-ratio silicon structure (μ foam) that facilitates sparse CNT growth and also enables uniform current emission. The devices were tested as field emitters in high vacuum (10^{-8} torr) and as electron impact ionizers using argon at pressures of up to 21 mtorr. The experimental data show that the MEMS extractor gate transmits up to 66% of the emitted current and that the ionizers are able to produce up to 0.139 mA of ion current with up to 19% ionization efficiency while consuming 0.39 W. [2009-0246]

Index Terms—Carbon nanotubes (CNTs), deep reactive ion etching (DRIE), electron impact ionization, field emission, gas ionizer, portable mass spectrometry (MS), 3-D MEMS packaging.

I. INTRODUCTION

MASS SPECTROMETERS are powerful analytical tools that are helpful to quantitatively determine the chemical composition of a sample. Unfortunately, conventional mass spectrometry (MS) hardware is bulky and power hungry, which limits the range of applications that the technology can satisfy. The development of gas chromatography and MS (GC/MS) systems that are small, light, mechanically and thermally robust, and low-powered would enable their portability and therefore would expand the applicability of MS techniques. Portable GC/MS systems, either as individual units or as part of massive networks, can be used in a wide range of *in situ* applications including geological survey, law enforcement, environ-

mental monitoring, process monitoring, and space exploration [1]–[12].

The four major components of an MS system are the ionizer, the mass filter, the detector, and the pump. The ionizer transforms the molecules of the sample to be analyzed into charged fragments, the mass filter sorts these ions according to their mass-to-charge ratios, the detector counts the number of ions that are transmitted by the mass filter, and the pump keeps the system at a pressure level that allows all the other subsystems to operate with good reliability [13]. The power consumption, size, and weight of an MS system are driven by the pumping requirements. Therefore, relaxation of the vacuum level at which the system components can operate is needed to enable its portability [14], [15].

There is a substantial number of reports in the literature on miniaturized MS components using conventional machining technologies, including fully functional MS systems [16], [17]. Even though these instruments achieve good performance, the machining methods involved in their making are relatively expensive, hence limiting the applicability of the technology. The use of micro- and nanofabrication technologies to implement miniaturized MS hardware would enable batch fabrication and would further reduce the dimensions of the components, which could lead to MS components with better performance. In addition, microfabricated technologies have the potential of implementing systems with higher performance through better component integration [18].

MEMS-based analytical instrumentation has been an active area of research for over three decades [19]. The literature on MEMS MS components concentrates on the development of scaled-down mass filtering hardware including time of flight [20], ion trap [21], ion mobility [22], and quadrupole mass filters [23]–[27]. There are also reports of MEMS detectors [28], [29] and MEMS vacuum pumps for portable applications [30]–[32]. The majority of the work reported on MEMS/NEMS ionizers is based on electron impact ionization. In an electron impact ionizer (EII), a stream of electrons is used to ionize neutral gas molecules by collision. Yoon *et al.* reported a MEMS ionizer that uses a tungsten filament [33], a nickel filament [34], or carbon nanotubes (CNTs) [34] as electron source, although no ionization data were provided. Other researchers have developed CNT-based EIIs suitable for portable MS, including Hwang *et al.* [35], Bower *et al.* [36], Chen *et al.* [37], Park *et al.* [38], and Manohara *et al.* [39]. In addition, low-power, portable, and high-pressure operation compatible glow-discharge ionizers have been developed. For example, Gao *et al.* reported spectra from a miniaturized glow discharge

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ionizer that is able to operate at 10^{-2} -torr-level pressures [40], and Longwitz *et al.* reported a MEMS glow-discharge ionizer intended for ion mobility MS [41]. However, these devices have reliability problems due to sputtering of the electrodes caused by the arc [42].

In an EII, the ratio between the ion current $I_I(E)$ and the electron current $I_E(E)$ is [43]

$$\frac{I_I(E)}{I_E(E)} = \frac{P}{K_b \cdot T} \cdot L \cdot \sigma_{\text{total}}(E) \quad (1)$$

where E is the energy of the electrons, P is the gas pressure, K_b is the Boltzmann's constant, T is the gas temperature, L is the ionization collision path length (i.e., the distance between the electron source and the ion collector), and σ_{total} is the total ionization cross section along the ionization collision path length. State-of-the-art EIIs for MS systems accomplish ionization using a thermionic electron source. Thermionic cathodes require high temperature (> 2000 K) to operate [44], which results in inefficient power consumption (typical thermionic ionizers consume 5 W), as well as in reliability issues if they are operated at high pressure ($> 10^{-4}$ torr) due to the chemical reactivity of the cathode surface, particularly if the gas mix contains oxygen [13], [45]. Therefore, thermionic-based EIIs are not ideal for portable MS.

A low-power approach to implement EIIs for portable MS systems involves the use of a cold cathode. Electrons are field emitted from the surface of metals and semiconductors when the potential barrier that holds electrons within the material (workfunction ϕ) is deformed by the application of a high electrostatic field [46]. High surface electrostatic fields are typically obtained by the application of a voltage to a high-aspect-ratio structure with nanometer-scaled tip radius. Field emission is described by the Fowler–Nordheim (FN) equation, which states that the electron current $I_E(V_G)$ emitted from a tip biased at a voltage V_G with respect to the gate has a strong dependence on the field factor β of the tip

$$I_E(V_G) \propto \beta^2 \cdot V_G^2 \cdot \exp \left[-\frac{B \cdot \phi^{1.5}}{\beta \cdot V_G} \right]. \quad (2)$$

In (2), B is equal to 6.8×10^7 when V_G and ϕ are in volts and the field factor is in per centimeter [46]. The field factor β relates the electric field at the surface of the tip to the applied gate voltage, and it is, to first order, equal to the inverse of the emitter tip radius r [47]. Field-emission cathodes have a faster response than thermionic cathodes because the time lag in a thermionic source is controlled by its thermal inertia, while the time lag in a cold cathode is set by the RC time constant involved in charging the electrodes.

The reliability of cold cathodes operated at high pressure is affected by back-ion bombardment and by chemical degradation. In the first case, back-streaming ions could impact and blunt the sharp tips, hence reducing their field factor. However, the addition of a second proximal gate to the basic field emitter diode configuration [48] could visibly mitigate back-ion bombardment if the second gate is biased at a more negative voltage than the bias voltage of the field emitter, effectively acting as collector of the back-streaming ions. In the second case, the

gas mix can contain reactive gases such as oxygen that could degrade the performance of the field emitters by increasing the workfunction of the emitting surface or by increasing the emitter tip radii due to the formation of a film on top of the emitter. In particular, chemical degradation due to oxygen strongly affects the performance of metallic field emitters, even for very low oxygen partial pressures [49].

Cold cathodes that use CNTs as field emitters are an attractive option in implementing EIIs for portable MS, because CNTs have large field factors due to their nanoscaled diameters and very high aspect ratios [50]. In addition, the remarkable physical properties and good chemical resistance of CNTs [50] address the back-ion bombardment-related and oxygen-related reliability issues of the EIIs. For example, Gadaska *et al.* reported single-gated CNT-based cathodes with long-term reliability while operating in an oxygen atmosphere at 10^{-4} -torr-level pressures [51]. Also, Chen *et al.* reported the operation of EIIs at mid millitorr-level pressures with no noticeable degradation using an array of double-gated CNTs as cathode [37]. In this paper, we report the operation of a CNT-based EII at 10^{-2} -torr-level pressure with no noticeable performance degradation and without using a second gate.

This paper is based on previously reported work from our group that explored sparse growth of CNTs without lithography for gas ionizers [52], [53]. This paper focuses on producing ions through electron impact ionization while providing a more comprehensive description of the technology. We also present new data that demonstrate more than two orders of magnitude larger ion current and almost a fourfold increase in the ionization efficiency than previously reported. Section II of this paper describes the structure and design of the CNT-based EII. Section III provides a description of the fabrication of the devices, and it also provides characterization of the fabrication using SEMs. Section IV presents and analyzes the data from the devices as electron field emitters and EIIs, and it compares the results with the literature.

II. DEVICE STRUCTURE AND DESIGN

The MEMS/NEMS CNT EII is composed of two substrates, i.e., a main body and an extractor gate, as shown in Fig. 1. Fig. 2 shows a set of SEMs that illustrate the structure of the main body substrate. The main body is a two-wafer stack that includes sparse arrays of plasma-enhanced chemical vapor deposited (PECVD) CNTs, a high-aspect-ratio silicon structure (μ foam), and a system of deep reactive ion etched (DRIE) springs to assemble the extractor to the main body. The extractor is a 1-cm-wide disk-shaped component with a perforated grid at its center, which includes a set of eight notches at its boundary. The notches are used to lock in the extractor substrate to the main body substrate, as shown in Fig. 3. The μ foam and the extractor grid do not line up, as shown in Fig. 4, because the extractor is used as a shadow mask during the deposition of the CNT catalyst (see Section III).

The MEMS/NEMS CNT EII includes a set of features to achieve low-voltage ionization, high-pressure operation, and device reliability. First, the device uses sparse forests of PECVD CNTs as field enhancers. The clusters of CNTs are

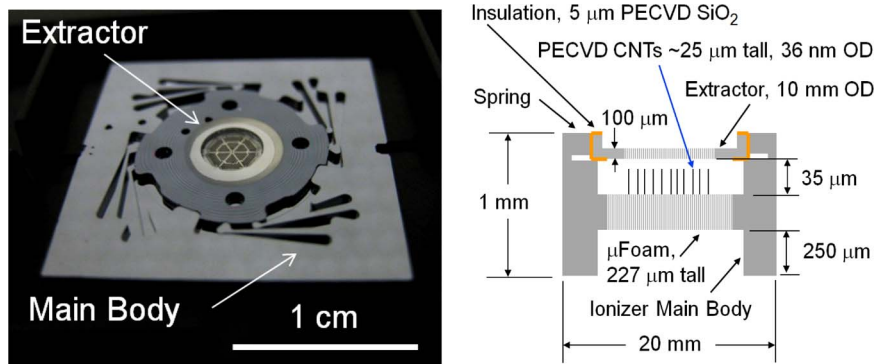


Fig. 1. (Left) Field view and (right) cross-sectional schematic of the MEMS/NEMS CNT EIL. The ionizer is composed of an extractor gate and a main body that includes a high-aspect-ratio silicon structure, sparse PECVD CNT forests as field enhancers, and a set of DRIE-patterned springs for assembly of the extractor gate. The boundary of the extractor has a 5- μm -thick PECVD silicon dioxide coating that provides electrical insulation between the extractor and the rest of the device. The vertical separation between the CNT tips and the extractor is about 10 μm . The boundary of the CNT forest is a 2-mm-diameter circle.

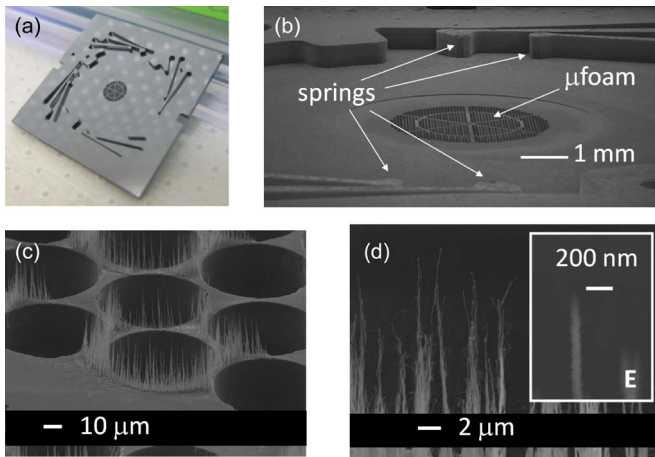


Fig. 2. (a) Field view photograph of the ionizer main body, (b) field view SEM of the central part of the main body showing the μfoam and the DRIE-patterned spring assembly system, (c) field view SEM of a sparse PECVD CNT forest on top of the μfoam , (d) close-up SEM of the PECVD CNTs, and (e) close-up SEM of a CNT tip. The typical CNT tip radius is about 36 nm.



Fig. 3. DRIE spring tip of the ionizer main body substrate locked into a notch on the boundary of the extractor substrate. The main body substrate has eight springs that are evenly distributed across the boundary of the extractor to assemble the extractor substrate to the rest of the ionizer.

sparse to diminish the shadowing effects due to the proximity between field enhancers. CNTs are resilient high-aspect-ratio graphitic structures with nanoscaled tip diameters ideal for field-emission applications [54], and they can operate as electron sources at high pressure ($> 10^{-4}$ torr). We had previously shown that it is possible to grow sparse clusters of PECVD CNTs without high-cost/low-throughput methods, such as e-beam lithography, by depositing the CNT catalyst (Ni) on

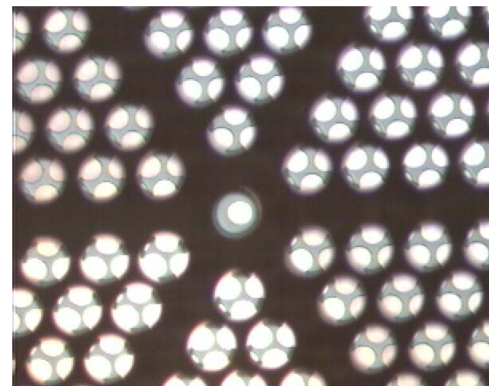


Fig. 4. Backlight top view of the extractor grid on top of the μfoam .

top of the μfoam [52]. Second, the device uses a MEMS proximal extractor gate that is held a few micrometers above the PECVD CNT tips. The proximal gate enables electron emission at low voltage. The extractor gate is integrated to the rest of the ionizer using a high-voltage MEMS packaging technology based on DRIE-patterned mesoscaled deflection springs [55]. We have shown that this packaging technology is capable of reversibly assembling mesoscaled parts with as little as 6.2 μm of biaxial misalignment and 0.6 μm of assembly repeatability, and it can also withstand large inertial forces without detaching [56]. This assembly approach makes it possible to use the extractor as a shadow mask to define the catalyst pads, then, to grow the PECVD CNTs without the extractor gate assembled, and, finally, to re-assemble the extractor to obtain a functional device. A distinct advantage of this approach is that it prevents plasma-induced damage of the silicon dioxide film on the surface of the extractor during CNT growth. The high degree of re-assembly alignment precision between the extractor and the CNT growth is reflected in the large electron current transmission through the extractor, which is almost two orders of magnitude larger than the electron current transmission of a nonaligned extractor using a similar emitting substrate and testing rig (see Section IV) [52]. Third, we previously showed that it is possible to achieve current limitation in the electron current if the device is made of a lowly doped silicon ($\sim 10^{13} \text{ cm}^{-3}$ doping concentration) [53]. The current limitation is presumably due to the high aspect

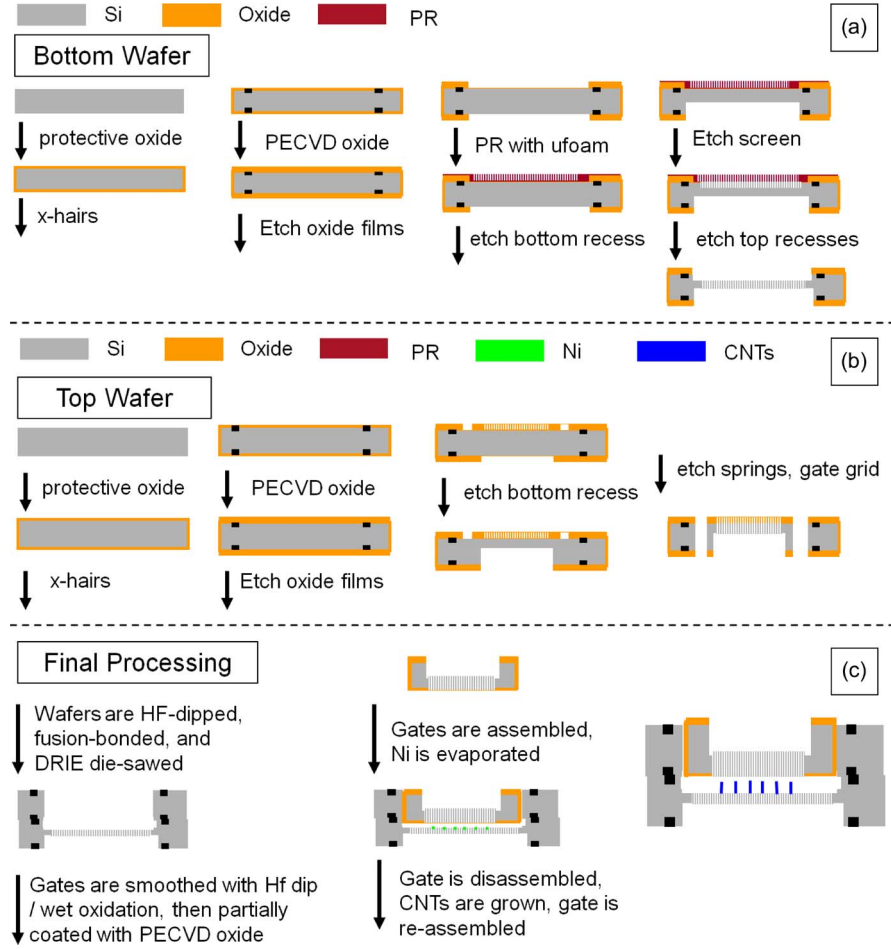


Fig. 5. Process flow to fabricate the MEMS/NEMS CNT EIIs: fabrication process flow of the (a) bottom wafer, (b) top wafer, and (c) final processing.

ratio of the μ foam, pinch off of the conduction channel due to the applied voltage, and the velocity saturation at high fields of electrons within the semiconductor lattice, resulting in more spatially uniform current emission [57], [58]. Finally, the design of the ionizer can potentially simplify the overall MS system integration by including a structure (i.e., the μ foam) that can serve as an interface between the interior and exterior of the MS. The μ foam acts as a perforated screen that feeds the exterior atmosphere into the MS at a rate controlled by the viscosity and the compressibility of the gas [59].

III. FABRICATION

The MEMS/NEMS EIIs are fabricated using two 6-in silicon wafers that are 500 μm thick. The two wafer substrates are used to fabricate the main body of the ionizers, while portions of one of the wafers are used to fabricate the extractor gates. The process flow in fabricating the bottom wafer is shown in Fig. 5(a), the process flow in fabricating the top wafer is shown in Fig. 5(b), and the final joint process flow is shown in Fig. 5(c). The initial fabrication steps of both wafers are identical. First, a 0.3- μm -thick thermal silicon dioxide film is grown on the wafers to protect their bonding surfaces from damage during processing. Then, alignment marks are etched on both sides of the wafers using contact lithography on a 1- μm -thick spin-coated photoresist (PR) and reactive ion etch-

ing (RIE) of the exposed silicon dioxide film and the silicon substrate directly underneath. Next, the PR is stripped off, and both sides of the wafers are coated with a 4- μm -thick PECVD silicon dioxide film that is annealed at 950 $^{\circ}\text{C}$ in nitrogen. The silicon dioxide films are used as hard masks in later etch steps. After the PECVD silicon dioxide films are annealed, the process flow continues as follows.

A. Bottom Wafer Process Flow

The silicon dioxide films on both sides of the wafer are etched using contact photolithography on 10- μm -thick spin-coated PR films and RIE. The oxide film on the top side of the wafer receives the features that create a recess in the main body for the CNTs, while the silicon dioxide film on the bottom side of the wafer receives the features that decrease the thickness of the substrate where μ foam is etched. Next, the PR films are stripped off, and the top side of the wafer receives the features of the μ foam using contact photolithography on a 10- μm -thick spin-coated PR film. After that, the bottom of the wafer is etched 250 μm using DRIE. The wafer is then mounted on a quartz handler wafer using PR, leaving the top side of the wafer readily accessible for processing. Next, the features of the μ foam are etched 220 μm deep using DRIE. The PR on the top side of the wafer is stripped off using an oxygen plasma, and a 30- μm deep etch is carried out both to complete the μ foam

and to create a recess for the CNTs. Finally, the silicon wafer is detached from the quartz wafer using acetone.

B. Top Wafer Process Flow

The silicon dioxide films on both sides of the wafer are etched using contact photolithography on 10- μm -thick spin-coated PR films and RIE. The oxide film on the top side of the wafer receives the features of the extractor gate (extractor grid and extractor boundary) and the assembly springs, while the silicon dioxide film on the bottom side of the wafer receives the features that decrease the thickness of the extractor gate where the grid is etched. Next, the PR films are stripped off. After that, the bottom of the wafer is etched 400 μm using DRIE. The wafer is then mounted on a quartz handler wafer using PR, leaving the top side of the wafer readily accessible for processing. Next, the features of the extractor grid, extractor boundary, and assembly springs are etched through the wafer. Once the extractor grid is fully etched, the region is protected with Kapton tape until the rest of the etch is completed. At the end of the DRIE step, the extractors are detached from the silicon substrate. Finally, the silicon wafer is detached from the quartz wafer using acetone, and the extractors are collected.

C. Final Processing

After the processing described in Section III-A and B, the wafers and the extractor gates are cleaned using a piranha bath, and then, they are dipped in 49% pure HF for 5 min to strip the silicon dioxide films. Next, the wafers and the extractor grids are RCA cleaned. The wafers are aligned and brought in contact for 12 hr while being pressed with 10 kN of force, and then, the wafer stack is annealed at 1050 $^{\circ}\text{C}$ in nitrogen for 1 hr. A 1- μm -thick thermal oxide film is grown on both the wafer stack and the extractor gates to smooth the DRIE-created surfaces. After that, the main bodies of the ionizers are die-sawed from the wafer stack using contact photolithography on a 10- μm -thick spin-coated PR, RIE of the silicon dioxide film, and DRIE of the silicon substrate. The extractor gates and main bodies of the ionizers are dipped in a 49% pure HF bath to strip the oxide film. Next, a 5- μm -thick PECVD silicon dioxide film is deposited on the boundary of the extractor gates using a shadow mask to avoid coating the extractor grid. After all this processing is completed, the extractor gate is assembled to the main body of the ionizer, and a 7-nm-thick e-beam evaporated Ni film is deposited on the μfoam using the extractor gate as a shadow mask. The extractor gate is then disassembled, and sparse forests of PECVD CNTs are grown on the μfoam using the PECVD CNT growth process described by Teo *et al.* [60]. The CNTs are about 25 μm long. Finally, the extractor gate is re-assembled to the main body.

IV. EXPERIMENTAL CHARACTERIZATION

Devices fabricated as described in Section III were tested as electron field emitters in high vacuum (10^{-8} torr) and as EIIs using argon at pressures between 5×10^{-7} and 2.1×10^{-2} torr. Analysis of the data demonstrates that the electron currents are

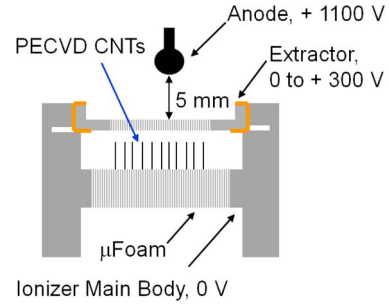


Fig. 6. Schematic of the field-emission testing setup.

field emitted by the CNTs and that the ionization data follow the electron impact ionization model. Ion currents that are as large as 139 μA , with 19% ionization efficiency, were measured.

A. Field-Emission Tests

Experimental Setup: Fig. 6 shows a schematic of the testing setup that is used to characterize the devices as electron sources. The test setup used a set of Keithley 237s controlled by Labview to bias voltages to the main body of the device, the extractor grid, and a suspended electrode, i.e., collector. This instrumentation is able to measure a maximum current of 10 mA and to apply a bias voltage between -1100 and $+1100$ V. When the main body of the device is biased at a negative voltage with respect to the extractor gate, electrons are field emitted from the CNTs on the main body, and a fraction of them is transmitted through the extractor gate. The collector is a 2-mm-diameter metallic sphere suspended about 5 mm above the extractor. The collector was biased at $+1100$ V during the field-emission tests. The collector allows us to discriminate between leakage current through the dielectric and electron emission, and it also let us estimate the electron current transmission of the extractor gate. If there is a linear dependence between the current intercepted by the extractor and the current collected by the suspended electrode, it can be concluded that both currents have the same physical origin. Since the collector has no direct electrical connection to the main body of the device, the origin of the measured currents cannot be the leakage current through the silicon dioxide that separates the extractor gate and the main body. We also conducted reverse-polarity tests to verify that the measured currents were field emitted, and we verified that the current emitted by the main body was equal to the grid current plus the collector current. In addition, we conducted FN analysis of the data to verify that the measured currents were field emitted by the CNT forests.

Field-Emission IV Characteristics: Depending on the doping concentration of the wafers used to fabricate the device, the device can emit anywhere between microampere-level [53] and milliamperere-level electron currents [52] for bias voltages between 100 and 450 V. In particular, Fig. 7 shows the IV characteristics of a device made of silicon wafers with a resistivity of $5 \Omega \cdot \text{cm}$. This device has a start-up voltage of about 150 V (emission current above 10^{-10} A), and it emits about 68 μA of electron current when biased at 348 V. Fig. 8 shows a plot of the current transmitted by the extractor gate versus the current intercepted by the extractor gate data shown in

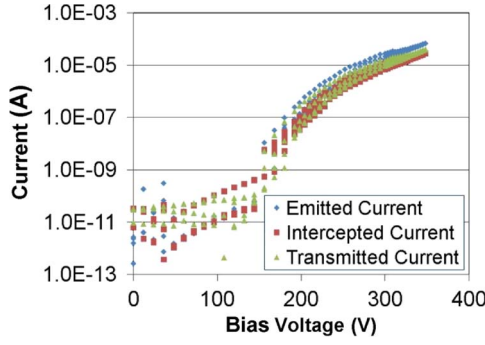


Fig. 7. Current emitted by the main body, current intercepted by the extractor, and current transmitted by the extractor for a MEMS/NEMS CNT ionizer made of $5\text{-}\Omega \cdot \text{cm}$ silicon wafers.

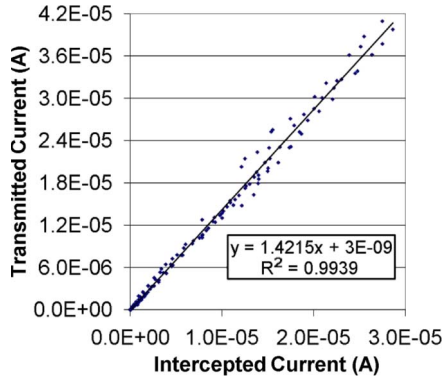


Fig. 8. Current transmitted by the extractor gate versus current intercepted by the extractor gate for the device with the data shown in Fig. 7.

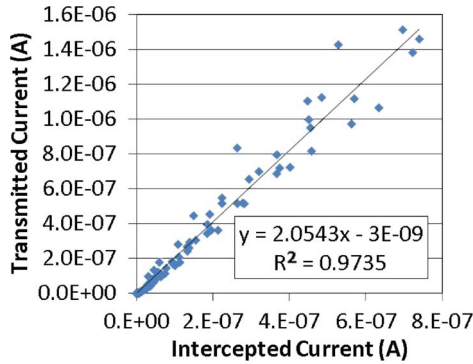


Fig. 9. Current transmitted by the extractor gate current versus current intercepted by the extractor gate for a CNT ionizer made with silicon substrates with a resistivity of $5\text{-}\Omega \cdot \text{cm}$. The transmission through the extractor is better than 66%.

Fig. 7. Fig. 8 clearly shows that there is a linear dependence between the two currents (correlation $R^2 > 0.99$), suggesting that the currents measured in the experiment are field emitted. Based on Fig. 8, we estimate a 59% transmission through the extractor gate of the electron current emitted by the main body. This value is more than an order of magnitude larger than the transmission of an unaligned extraction gate using a similar emitting substrate [52]. We have tested devices with as much as 66% electron current transmission through the gate while emitting less current, as shown in Fig. 9.

FN Analysis: An analysis of the IV characteristics of the device would help us further corroborate that the measured

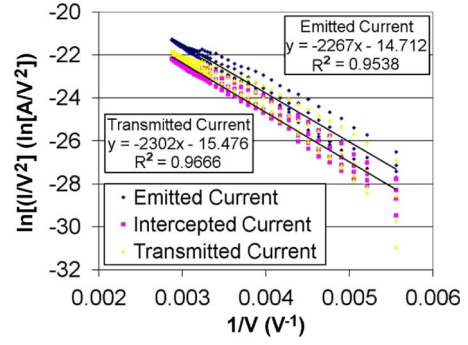


Fig. 10. FN plot of the data shown in Fig. 7. The transmitted, intercepted, and emitted current data describe the parallel straight lines in the FN plot.

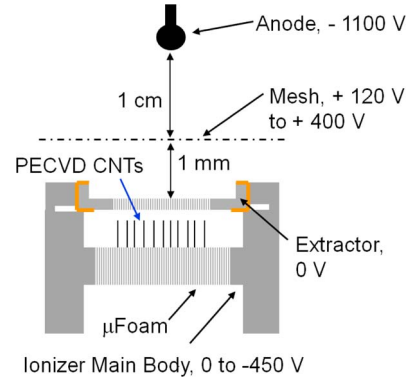


Fig. 11. Schematic of the electron impact ionization testing setup.

currents were field emitted by the CNT forests. Fig. 10 shows the FN plot ($\ln[I/V_G^2]$ versus V_G^{-1}) of the data shown in Fig. 7. The data in the FN plot describe parallel straight lines that evidence good correlation with the FN model ($R^2 > 0.95$). An average field factor β of $3 \times 10^5 \text{ cm}^{-1}$ is estimated from the slope of the FN plot, assuming that the workfunction for graphite is 4.8 eV. As a conservative estimate, $\beta \sim r^{-1}$ [47]. Therefore, a CNT tip radius of 33.3 nm is estimated from the FN plot, which is in good agreement with the 36-nm tip radius estimate from SEMs [see Fig. 2(e)].

B. Electron Impact Ionization Tests

Experimental Setup: Fig. 11 shows a schematic of the testing setup used to characterize the devices as EIIs, with argon at pressures between 5×10^{-7} and 2.2×10^{-2} torr. We measured the pressure with a Bayard–Alpert gauge from Varian, Inc. (Lexington, MA), which was directly connected to the vacuum chamber, and it is able to measure between 5×10^{-10} and 3×10^{-2} torr. The electron impact ionization testing setup is very similar to the field-emission testing setup shown in Fig. 6, although the electron impact ionization setup includes a fourth electrode. In the electron impact ionization tests, the main body of the device is still biased negatively with respect to the extractor gate to field emit electrons (the extractor was biased at 0 V, while the main body was biased at a voltage between 0 and -530 V). The fourth electrode is a high-transparency mesh that is biased at a positive voltage with respect to the extractor gate. The mesh attracts the electrons that are transmitted by the extractor gate to make them ionize the surrounding neutral

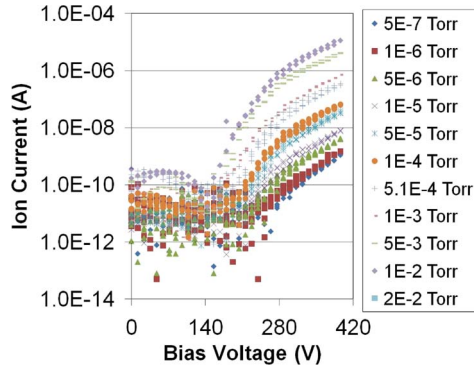


Fig. 12. Ion current versus emitter-to-extractor bias voltage of the ionizer with the field-emission data shown in Fig. 7.

gas molecules by collision. The suspended collector electrode is biased at a negative voltage to attract the ions that are formed in the ionization region. The collector was usually biased at -1100 V during the electron impact ionization tests, but we did not see any appreciable difference in the data as long as the collector voltage was negative. We typically biased the mesh electrode at $+400$ V with respect to the extractor electrode, but we did not see a substantial variation in the data when we varied the mesh electrode voltage down to $+120$ V. Our experimental setup does not do a good job of defining a constant-potential region where the ionization takes place (ideally, the mesh electrode should be a high-transparency cage), which would allow us to choose a potential that maximizes the total electron impact ionization cross section. Instead, the space between the mesh and the collector electrodes has a nonzero electric field that locally varies the electric potential, which defines the local total electron impact ionization cross section.

IV Characteristics: Fig. 12 shows the electron impact ionization data of the device with the field-emission data shown in Fig. 7. In the data, the pressure was varied between 5×10^{-7} and 2×10^{-2} torr, and the bias voltage between the main body and the extractor was varied between 0 and 396 V. From Fig. 12, it can be seen that the constant-pressure *IV* characteristics are parallel curves, as expected from the linear dependence of the ion current on the gas pressure for a fixed electron current shown in (1). In this data set, the device operated at a maximum pressure of 20 mtorr, and it produced up to $22.5 \mu\text{A}$ of ion current while emitting $140 \mu\text{A}$ of electron current (55-mW power consumption and an ion-to-electron current ratio of 0.16). We also tested devices that produced larger ion currents. For example, Fig. 13 shows the ion and electron current versus the extractor bias voltage data at 21 mtorr from a device in the same batch as the device previously discussed. The data were collected while conducting a series of six extractor voltage sweeps. The ionizer produced a maximum ion current of 0.139 mA while emitting 0.738 mA of electron current during the last voltage sweep of the series (0.39-W power consumption and an ion-to-electron current ratio of 0.19). It is clear from Fig. 13 that there was a sudden change in the *IV* characteristics of the device in the last sweep, i.e., more ion and electron currents for the same extractor bias voltage. The change in the *IV* characteristics was not permanent because we were able to duplicate the data shown in Fig. 13 whenever we conducted

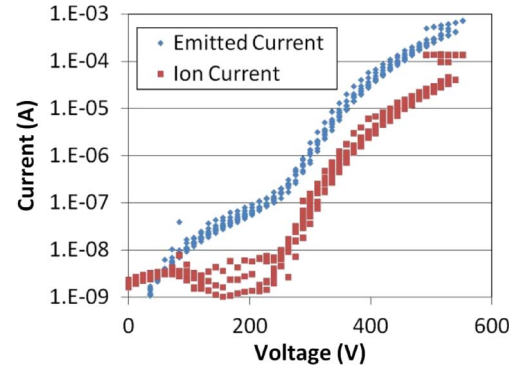


Fig. 13. Ion current versus emitter-to-extractor bias voltage of an ionizer made from silicon wafers with $5\text{-}\Omega \cdot \text{cm}$ resistivity. The data were taken with argon at a pressure of 21 mtorr.

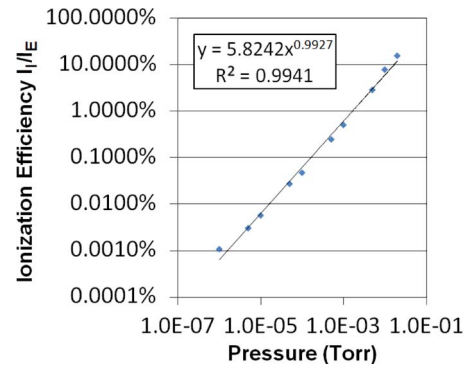


Fig. 14. Ion-current-to-electron-current ratio versus pressure of the data shown in Fig. 12.

a series of multiple sweeps of the extractor bias voltage after letting the ionizer be inactive for a few minutes. Therefore, the experimental behavior of the *IV* characteristics suggests some kind of hysteresis. We speculate that the reversible increase in the field factor of the ionizer is due to an effect that is analogous to the well-known pull-in effect in surface micromachined MEMS [18]. The increase in the field factor could have been caused by the reduction of the separation between the CNT tips and the extractor due to buckling of the extractor grid, which could have been caused by the thermal expansion of the grid due to the electron current that it intercepted (a peak power of about 0.16 W was dissipated by the extractor gate) and of the electrostatic suction of the extractor gate toward the main body due to the bias voltage.

C. Discussion

Equation (1) can be seen as an ionization efficiency, i.e., as the ratio of the number of ions formed to the number of electrons used in the ionization process [61]. Fig. 14 shows a plot of the ion-current-to-electron-current ratio at 396 V versus the pressure of the data shown in Fig. 12. A fit of the data shown in Fig. 14 suggests that the data follow the electron impact ionization model because the current ratio is proportional to the pressure to the 0.993 power, the correlation R^2 of the fit is better than 0.99, and the 5.82 coefficient of the fit implies an average total ionization cross section of $1.95 \times 10^{-16} \text{ cm}^2$, which is within the range of total ionization cross sections for the range

of voltage that are present in the ionization region [62]. The data shown in Fig. 12 have a maximum ionization efficiency of 16%, while the data shown in Fig. 13 have an ionization efficiency of 19%, which is several orders of magnitude higher than the ionization efficiency that is typically attained by thermionic EIIs [13]. Looking at (1), it is possible to implement field-emission-based and thermionic-emission-based ion sources with the same (and optimal) ionization efficiency at a given pressure if the effective ionization length is the same and the effective ionization cross section is maximum. The key advantage of the CNT-based EII that we report over the thermionic-based EIIs is its capability to operate at orders of magnitude higher pressure than the thermionic-based EIIs without degradation, which leads to the capability to achieve orders of magnitude of higher ionization efficiency, in combination with the resistance to oxygen that CNT-based cold cathodes has, as other researchers have shown [51]. If one uses a differential pumping scheme where the thermionic electron source is held at high vacuum but the ionization region is held at a higher pressure, then it is arguable that the thermionic-based EIIs would perform just as well as the CNT-based EIIs. However, this architecture clearly introduces more complexity to the portable MS design, and it would visibly increase the energy required for pumping.

While conducting the electron impact ionization tests, we tried multiple times to collect data at pressures higher than 21 mtorr, but instead, the pressure increase always led to the creation of a plasma within the vacuum chamber triggered by the high voltage biased to the spherical electrode. However, as we had previously reported [52], the devices did not suffer any noticeable loss of performance after striking a plasma. Nonetheless, it is possible to burn out some of the CNTs and, hence, shift the *IV* characteristics of the device if the bias voltage between the extractor gate and the emitters is large enough. Fortunately, we showed in a previous report that it is possible to limit the emitted electron current [53], which should reduce the likelihood of field emitter burn-out.

Our ionizer compares favorably to other MEMS/NEMS-enabled ionizers reported in the literature. For example, the electron current of our devices is several orders of magnitude larger than what was reported by Liu *et al.*, with nanostructured boron nitride cathodes, and their operational voltage is visibly higher [63]. Also, the electron current that our CNT-based ionizer attained is several orders of magnitude larger than the electron current achieved by the MEMS thermionic ionizers developed by Yoon *et al.* (7 nA using a tungsten filament [33] and 280 nA using a nickel filament [34]) or several orders of magnitude larger than the electron current from the CNT-based cold cathodes reported by Yoon *et al.* (18.5 μ A at 175 V [34]) or Hwang *et al.* (80 μ A at 1.1 kV [35]). Moreover, the ionization efficiency of our devices is almost four times larger than the ionization efficiency reported by Chen *et al.* using arrays of double-gated isolated PECVD CNTs, with argon at 5.6 mtorr [48]. In addition, our devices have an estimated field factor from the FN plot that is 50% larger than the corresponding values reported by Manohara *et al.* [39], which implies that our ionizer can extract more current and, hence, produce more ion current with less bias voltage. We speculate that the smaller field factor in the work of Manohara *et al.* is related to the

shadowing effect that their CNTs suffer due to their proximity within the same fiber. Finally, Bower *et al.* reported a very interesting CNT-based EII with 30- μ m CNT tips-to-extractor separation and 280- μ m extractor-to-collector separation (i.e., ionization length) [36]. They reported operation of their EII at pressures as high as 0.1 torr. Using Ar, Bower *et al.* reported an ionization efficiency of 0.009 at 20 mtorr and 0.05 at 0.1 torr. Our ionization efficiency at 20 mtorr with the same gas is more than 20 times larger. We believe that the ionization efficiency of their EII is visibly smaller than the ionization efficiency of our EII at the same pressure and with the same gas, because their EII has a very short ionization length. Based on their data, we estimate that their EII would have a similar ionization efficiency if its ionization length would be as large as ours.

V. CONCLUSION

We reported the fabrication and experimental characterization of a PECVD CNT-based MEMS/NEMS electron impact gas ionizer with an integrated extractor gate for portable MS. The extractor gate is integrated to the ionizer using a set of DRIE springs that are capable of extractor gate re-assembly with micrometer-scale precision. The devices were tested as field emitters in high vacuum (10^{-8} torr) and as EIIs using argon at pressures of up to 21 mtorr. The ionization data follow the electron impact ionization model. The extractor gate transmits up to 66% of the emitted electron current, and the devices produced up to 0.139 mA of ion current, with 19% ionization efficiency, while consuming 0.39 W.

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